

FORM PTO-1449 (Modified)

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket No. A1WI2376US

Application Number

Applicants: Douglas W. Babcock, Robert A. Duris, Bruce Hecht

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

a	1.	Three technologies on one chip make a broadband amplifier + "T" coil, by John Addis, Tektronix Inc., Beaverton, Ore., Electronics/June 5, 1972
	2.	Modified T-coil Network, Jim Hagerman, Hughes-JVC Technology Corp., 2310 Camino Vida Roble, Carlsbad, CA 92009; Internet: 72230.1704@compuserve.com , 9/12/95
a	3.	Small-Signal MMIC Amplifiers with Bridged T-Coil Matching Networks, L. Solmi, D.B Estreich, and B. Ricco, IEEE Journal of Solid-State Circuits, Vol. 27, No. 7, July 1992, 0018-9200/92\$03.00© 1992 IEEE
	4.	Good Engineering and Fast Vertical Amplifiers, John Addis, Analog Circuit Design, Butterworth-Heinemann, pages 107-122, 1991.

Examiner

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

